



<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Atty. Docket No. <b>010704</b>	Serial No. <b>09/871,810</b>
	Applicant(s): <b>NAKATA, Shunji et al.</b>	
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### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<u>C7E</u>	AA	5,521,538	Dickinson	05/28/1996	326	93
_____	AB					
_____	AC					
_____	AD					
_____	AE					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
<u>C7E</u> AF 8-335873	12/17/1996	Japan	Abstract
_____ AG			
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### OTHER DOCUMENTS

<u>C7E</u>	AI	Athas, William C., "Energy-Recovery CMOS", Low power Design Methodologies, J. M. Rabaey and M. Pedram (Kluwer Academic Publishers, 1996), Chapter 4, pages 65-72.
<u>C7E</u>	AJ	Athas, William C. et al., "A Low-Power Microprocessor Based on Resonant Energy", IEEE Journal of Solid-State Circuits, Vol. 32, No. 11, November 1997, pages 1693-1701.
<u>C7E</u>	AK	"Low Power and High Speed LSI Technology", Realize Co., along with partial translation of relevant parts, January 31, 1998.

Examiner <u>Cassandra Cox</u>	Date Considered <u>6/12/03</u>
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